

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,042,280 B1
APPLICATION NO. : 10/737254
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INVENTOR(S) : Shengming Huang et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7, Line 48: "transistor is operates" should read --transistor is operated--

Column 8, Line 58: "transistor is operates" should read --transistor is operated--

Column 9, Line 37: "drain that is couple" should read --drain that is coupled--

Column 9, Line 45: "is includes a gate" should read --includes a gate--

Column 10, Line 46: "(I-Vmin_OD/1L)" should read --(I-Vmin_OD/RL)--

Signed and Sealed this

Twenty-sixth Day of September, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office